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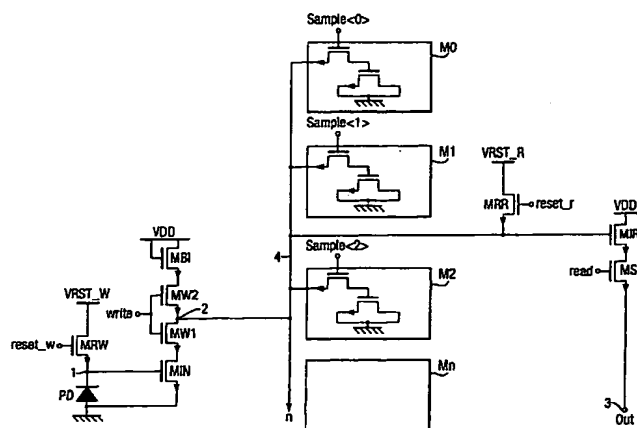
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(54) Title: **IMAGING DEVICE**



(57) Abstract: An imaging device comprising a two-dimensional array of pixels which are scanned to build up a desired image. Each pixel comprises a sensor such as a photodiode PD which outputs a signal dependent upon the strength of the incident radiation. This signal is amplified in an inverter circuit MB1, MIN and switched by transistors MW1, MW2 which in turn are controlled by a write signal applied to their connected gate electrodes. During the write period, the output signal from photodiode PD is passed to a line (4) which is input to a number n+1 of memory cells M0 to Mn which are switched sequentially via respective control inputs sample to sample to receive and store respective samples of the amplified signal from photodiode PD. The stored samples are read out sequentially over a read period during which a read transistor MSE, controlled by a read signal at its gate electrode, connects the memory cells to an output terminal (3) connected to the column bus.

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